***Institute of Computer And Technology***

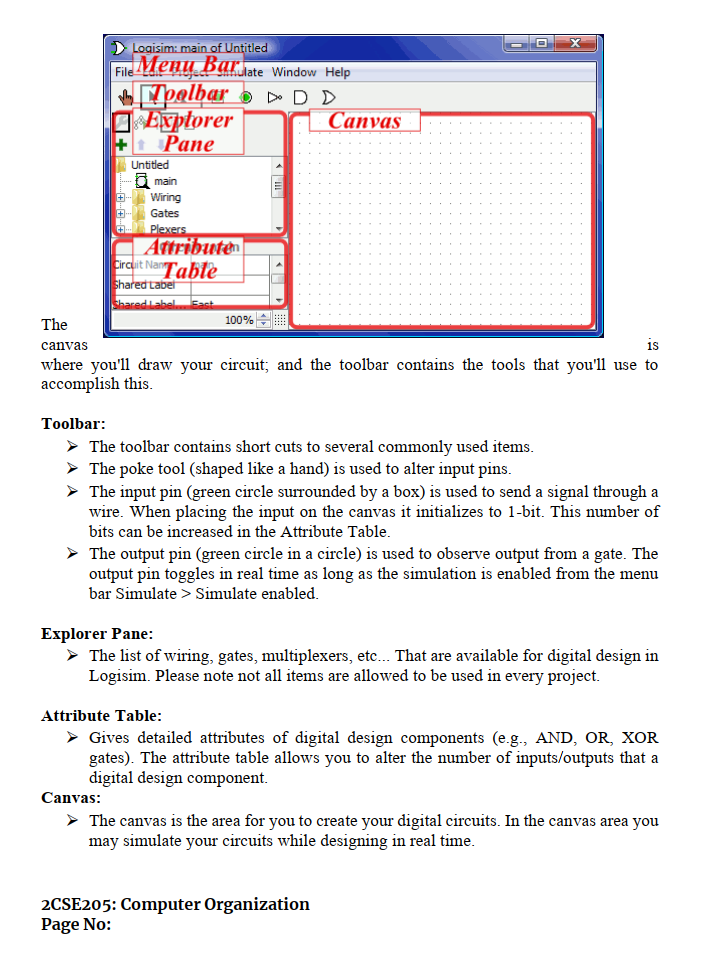
***B.Tech – CSE(BDA)***

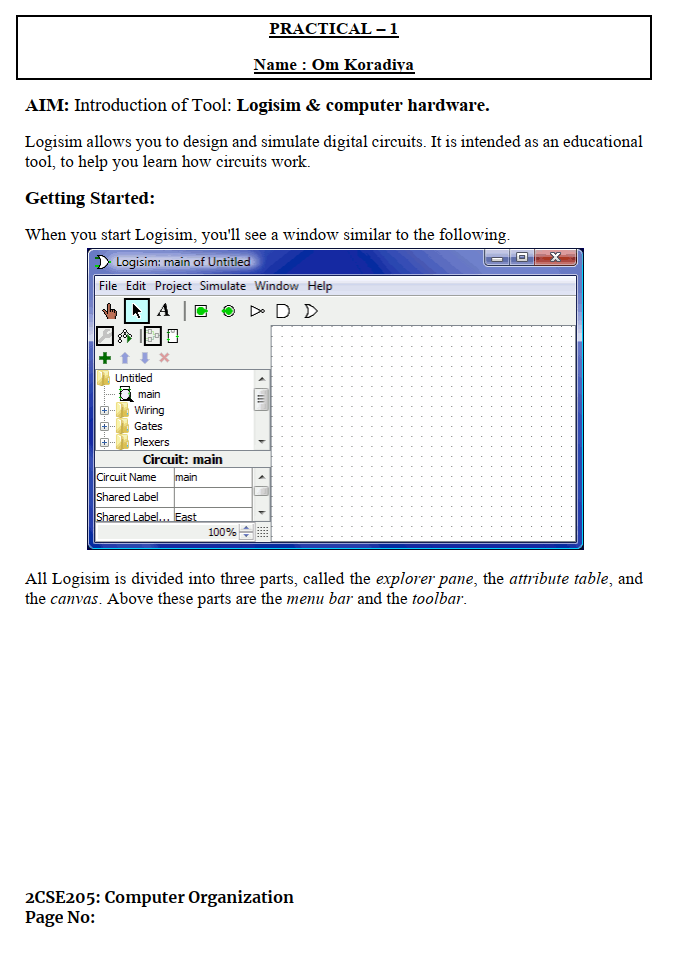
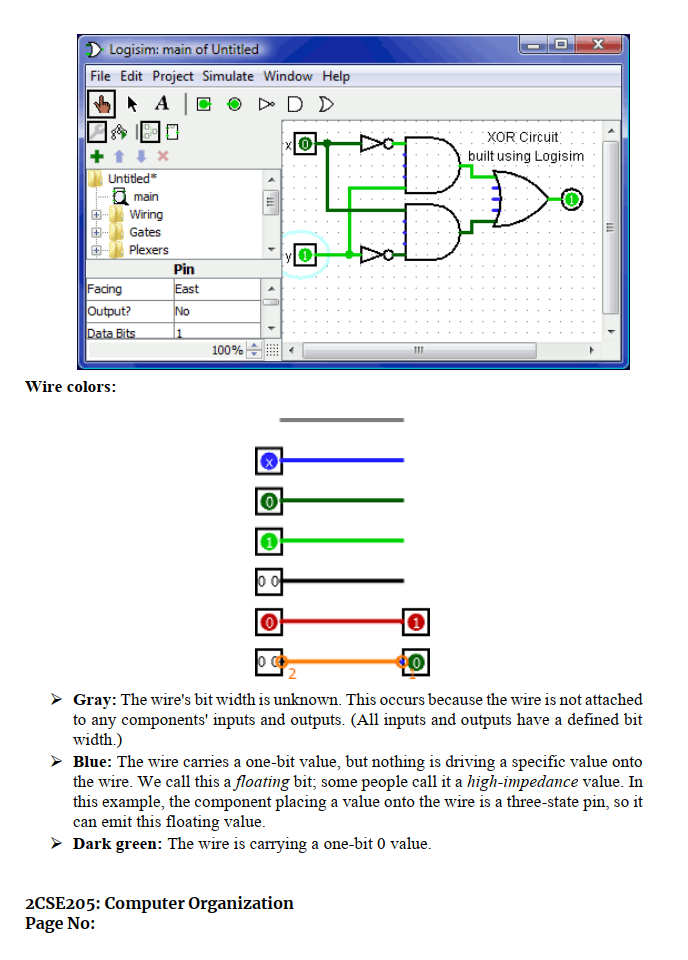
***Name:- Dwij Vatsal Desai***

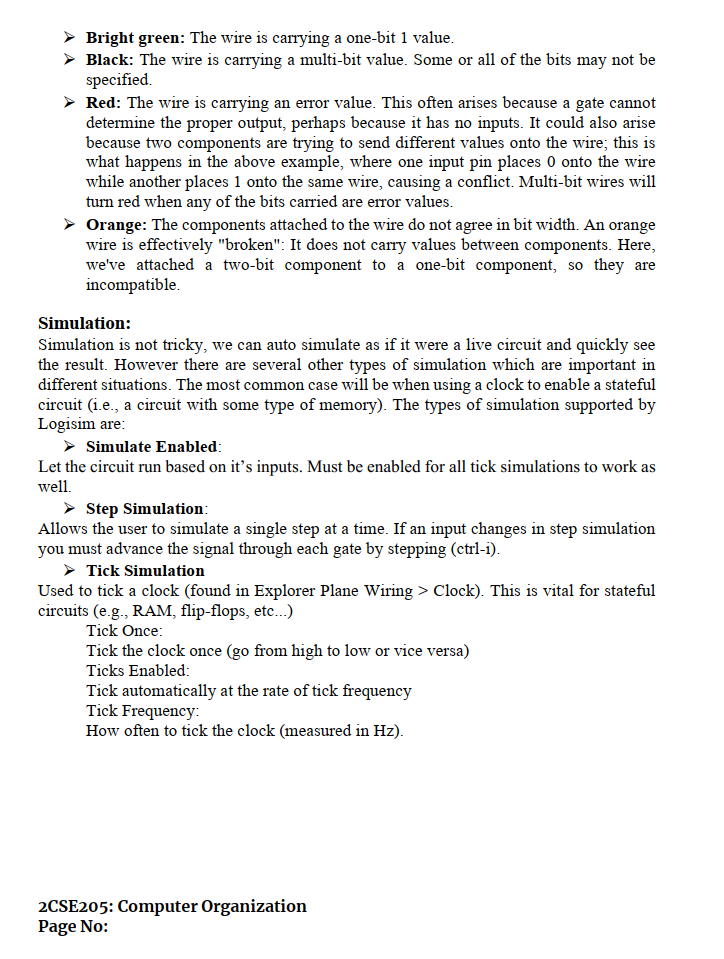
***Sem:- 2 Sub: - CO***

***Enrollment No.:- 23162121027***

***Prac:- 1 Date:- 10/2/2024***

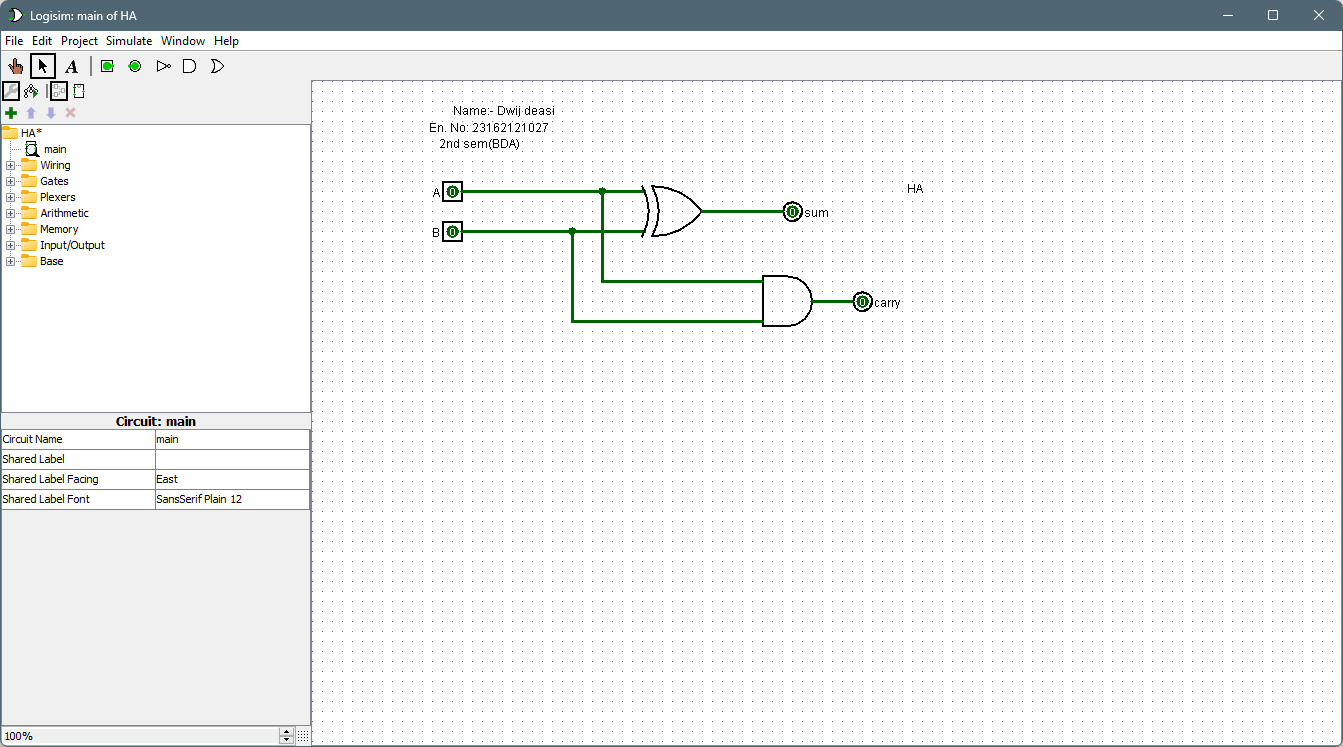
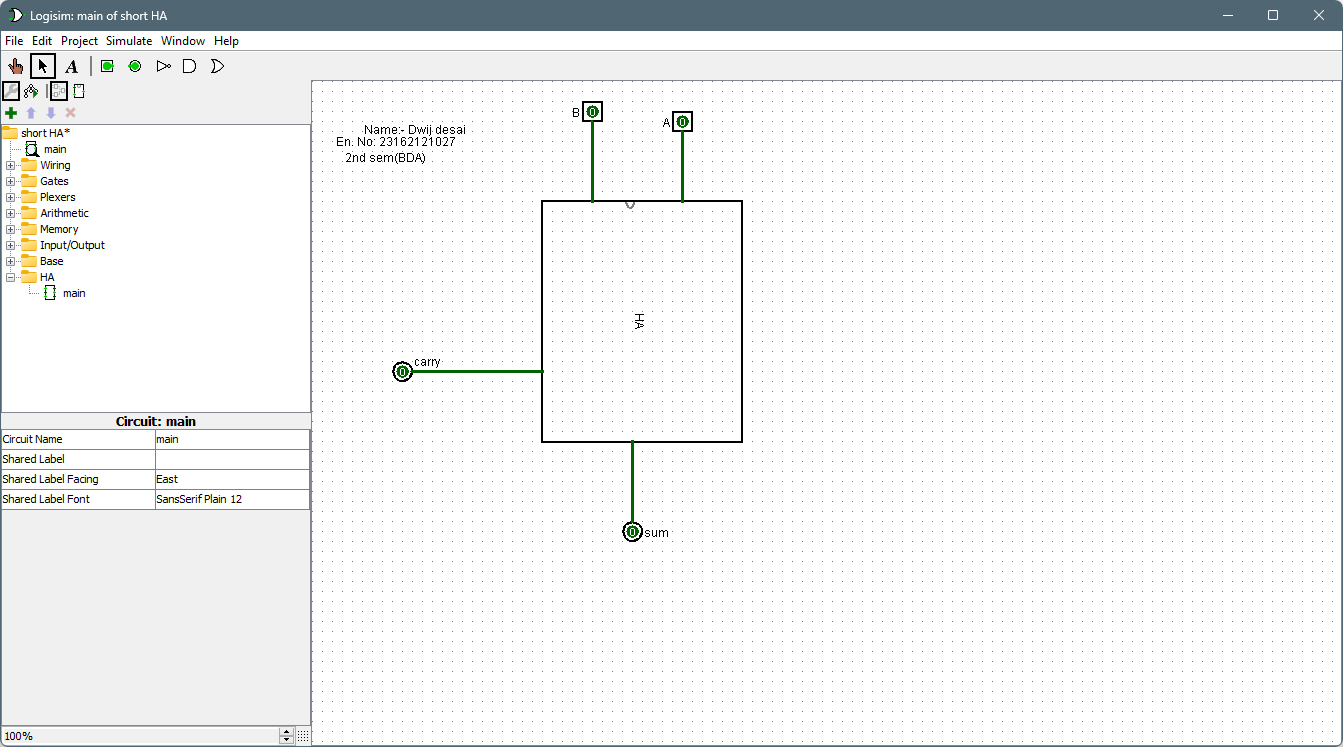


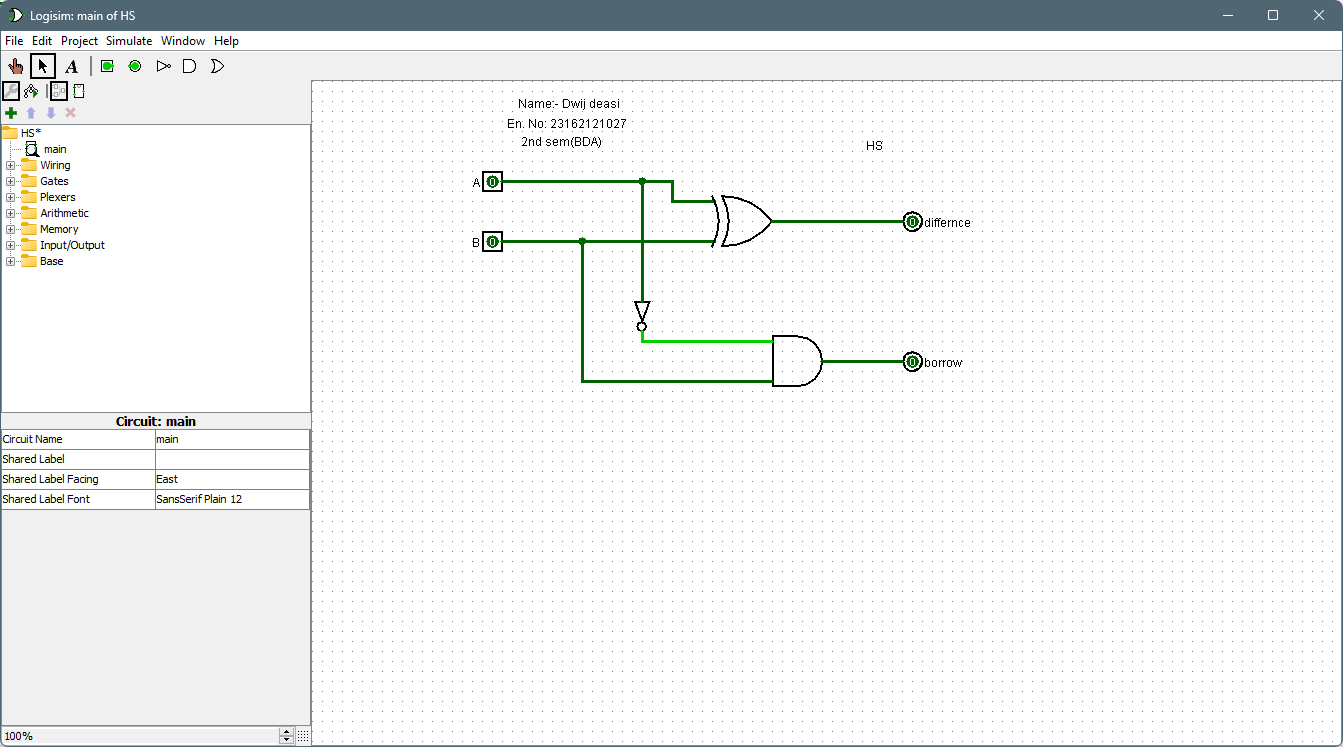


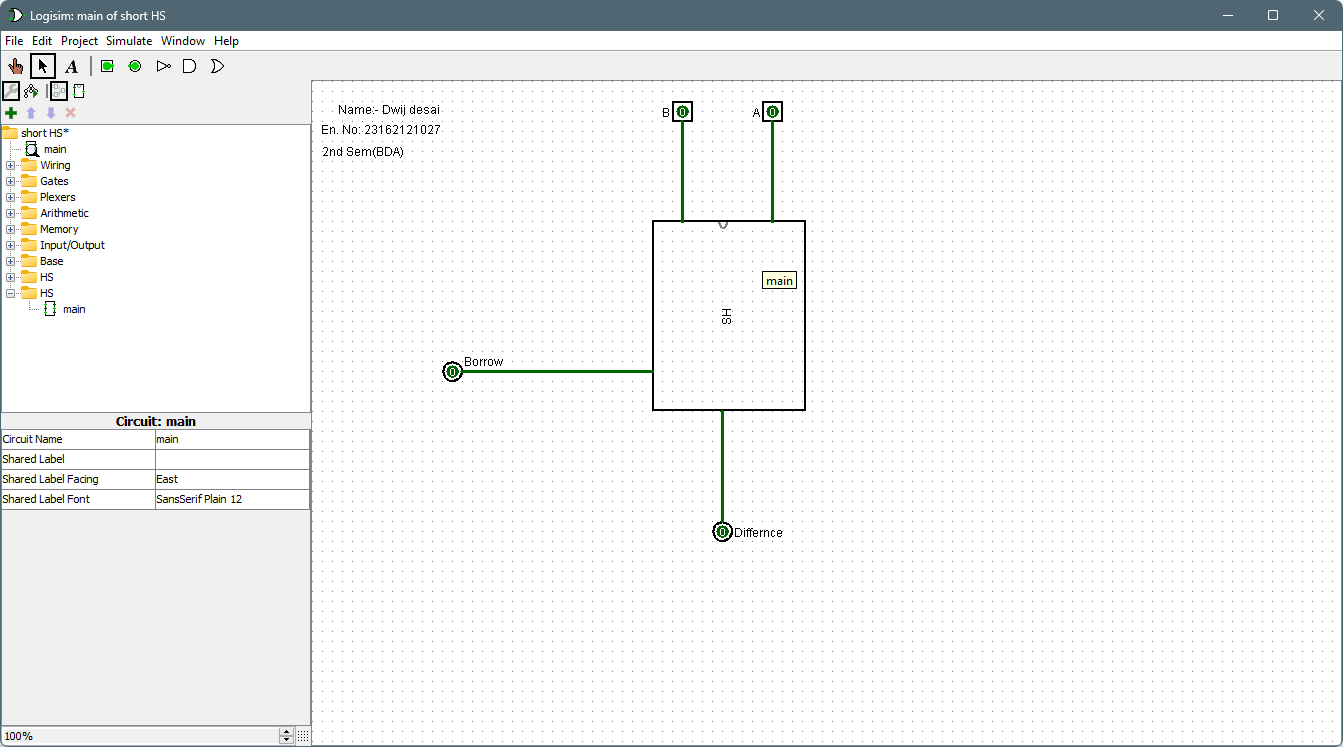
**Exercise:**

1. Half Adder:-

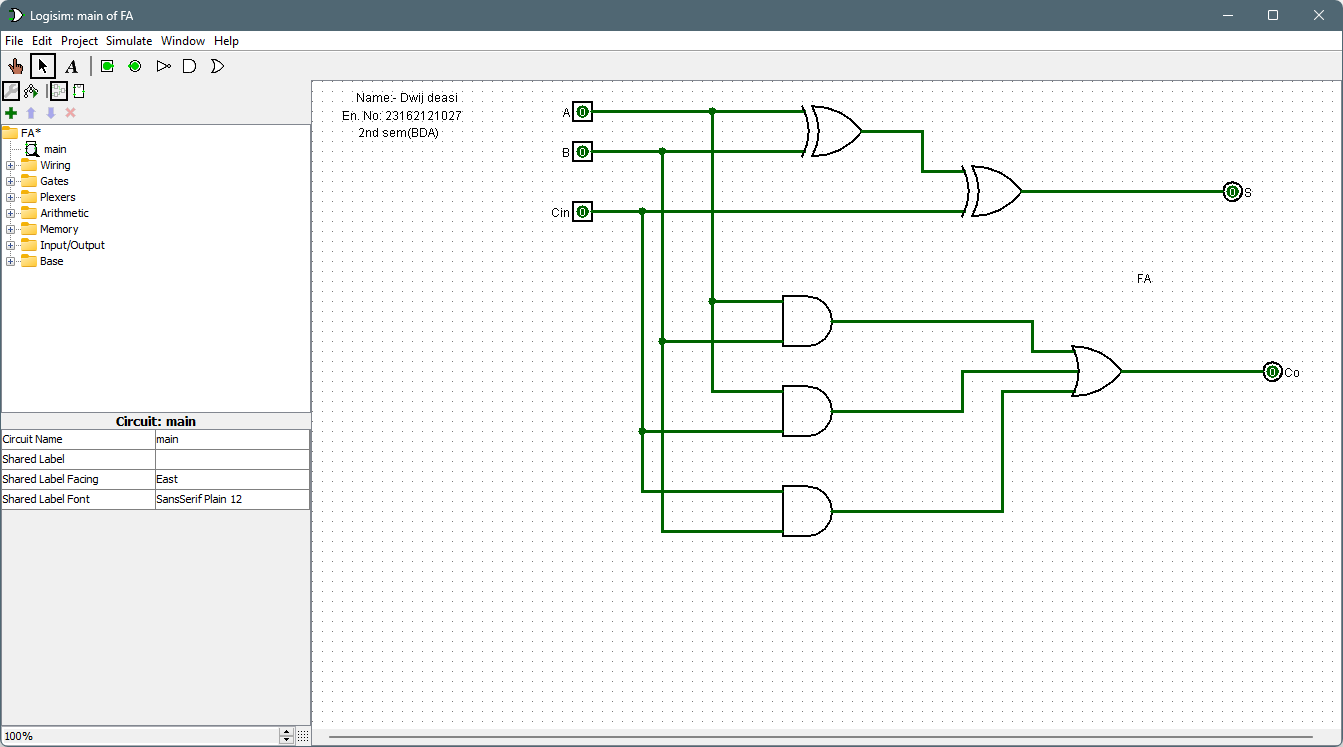
 

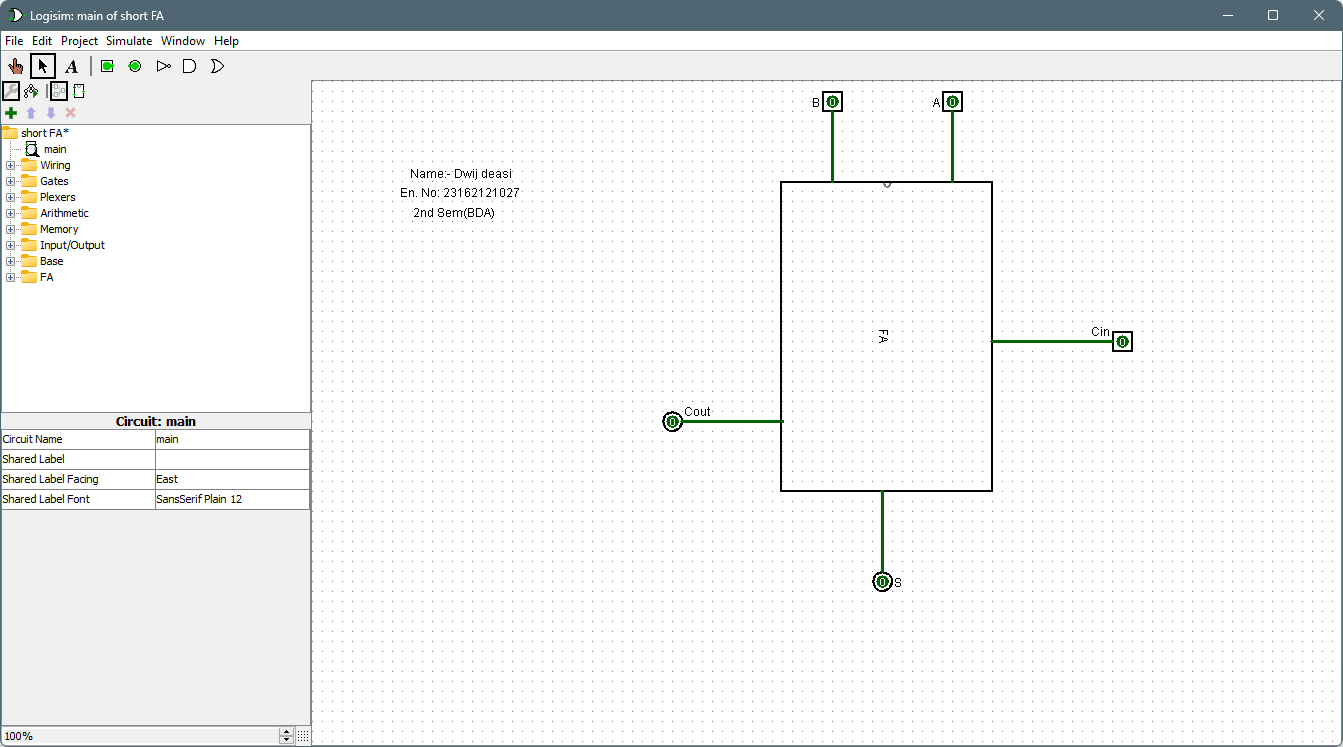
1. Half Subtractor:-



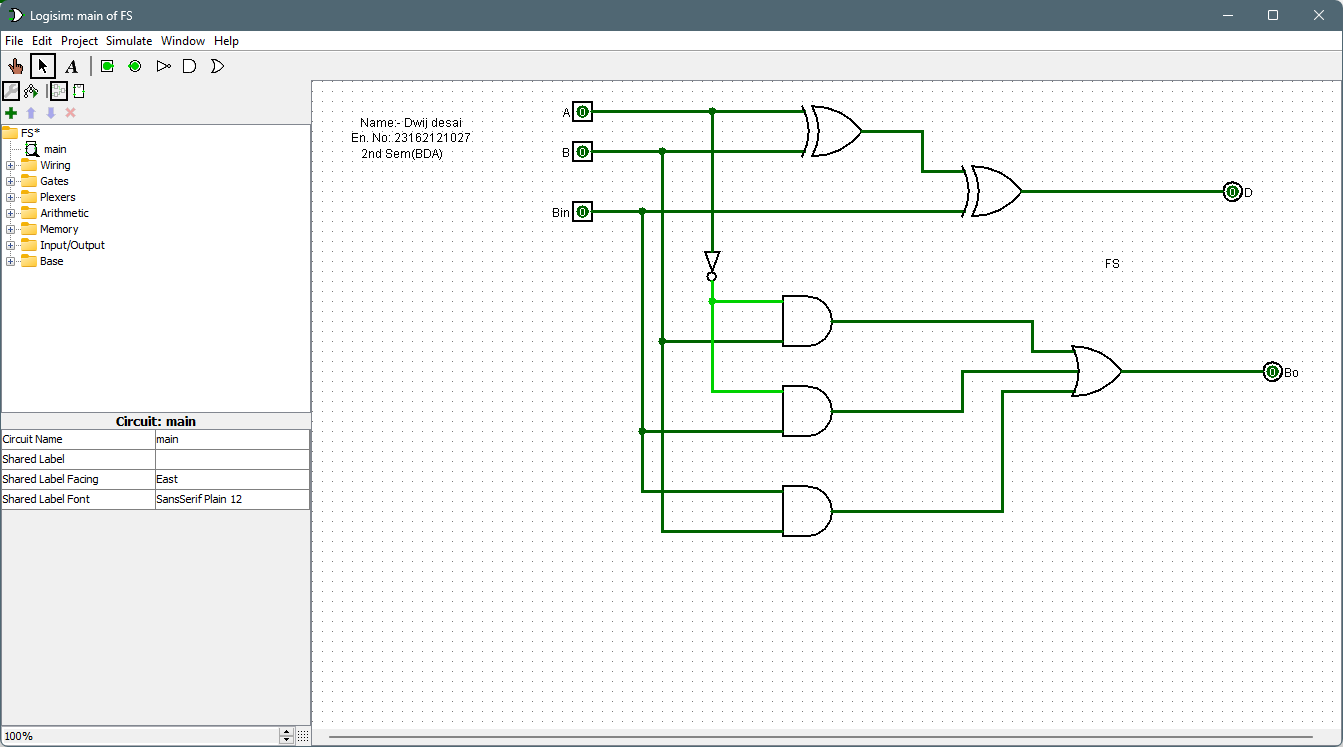


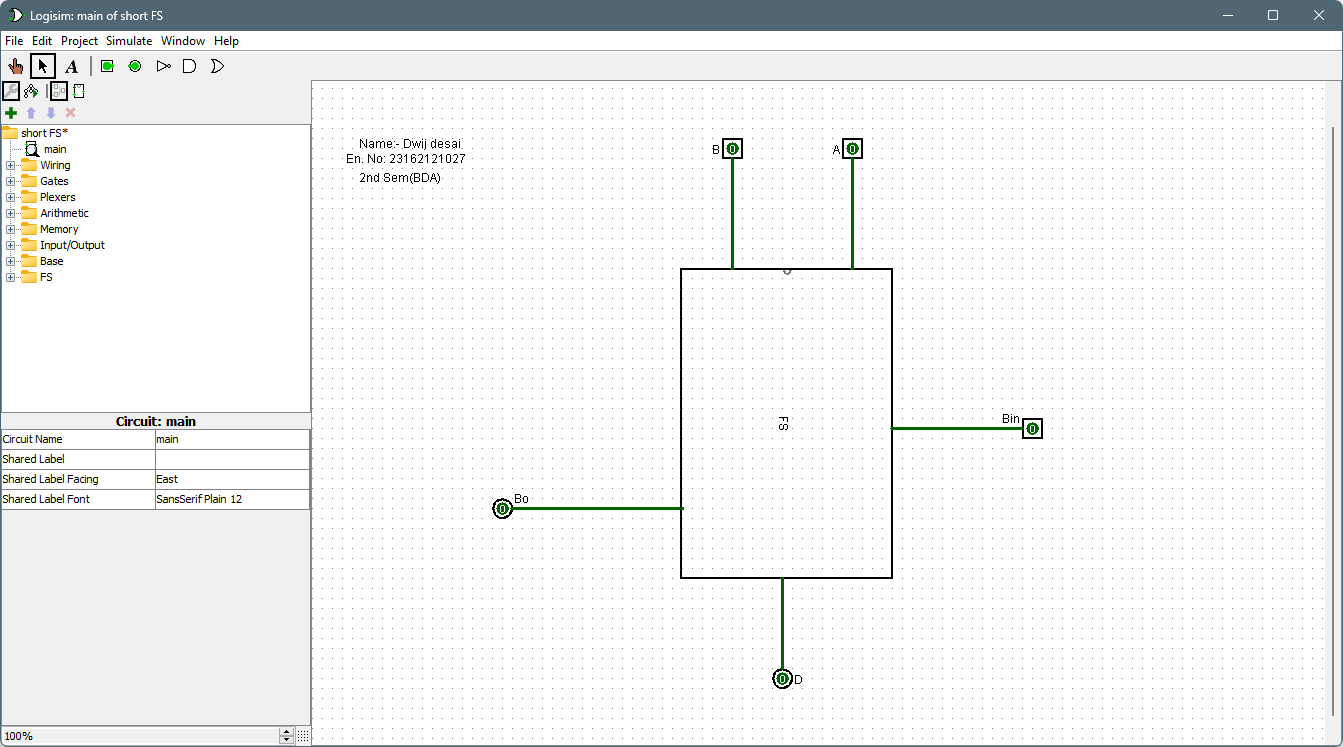
1. Full Adder:-



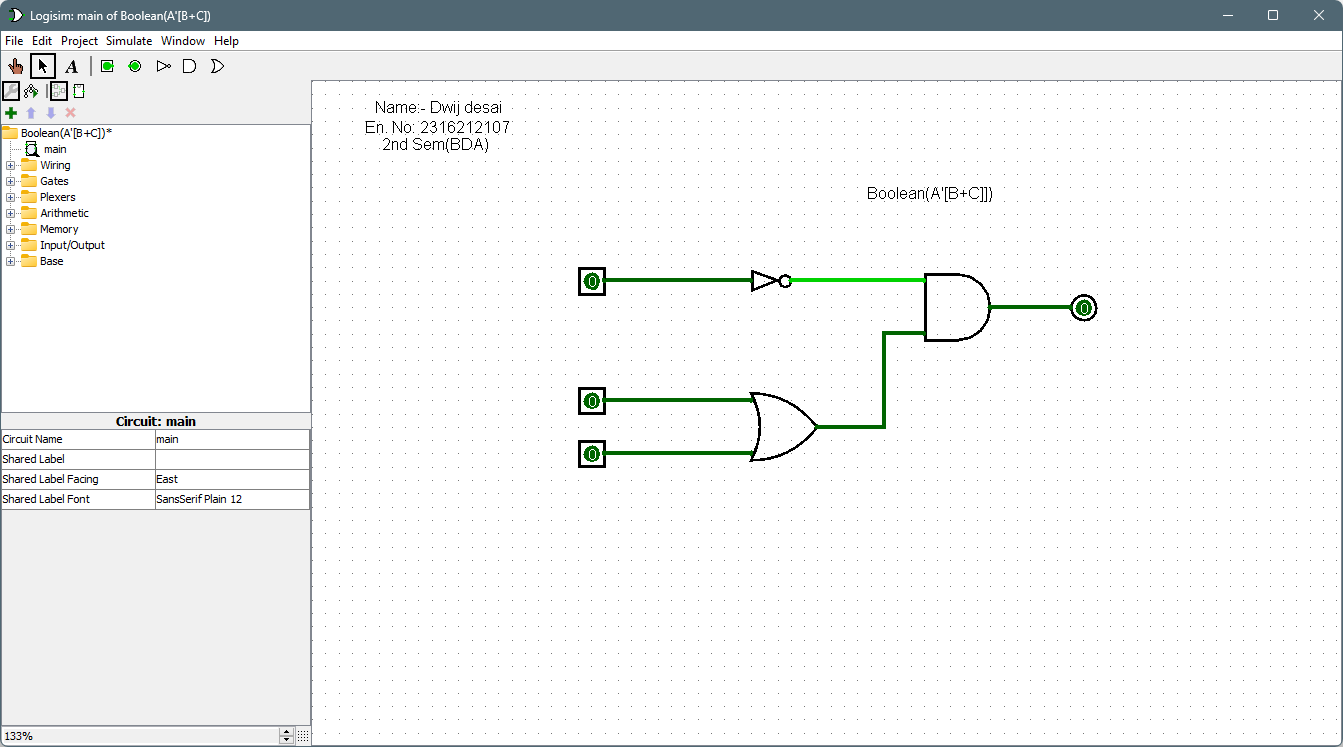


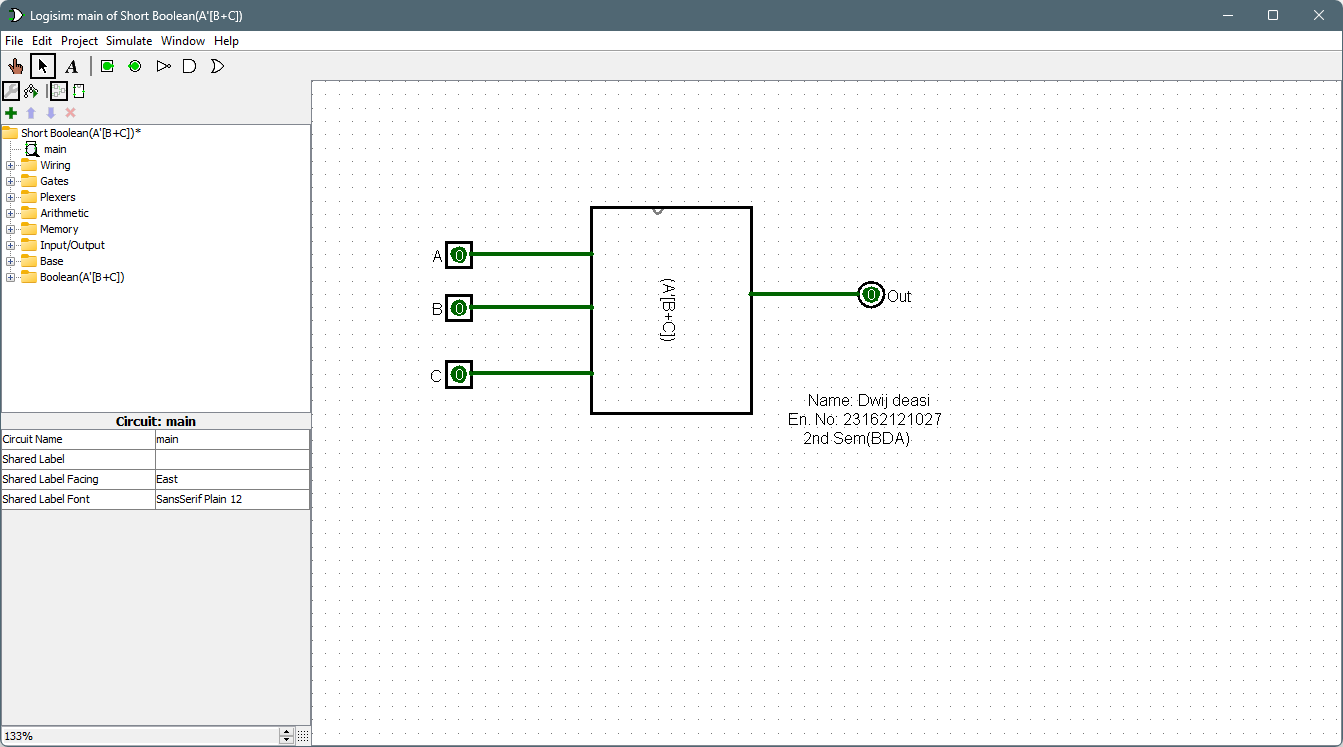
1. Full Subtractor:-



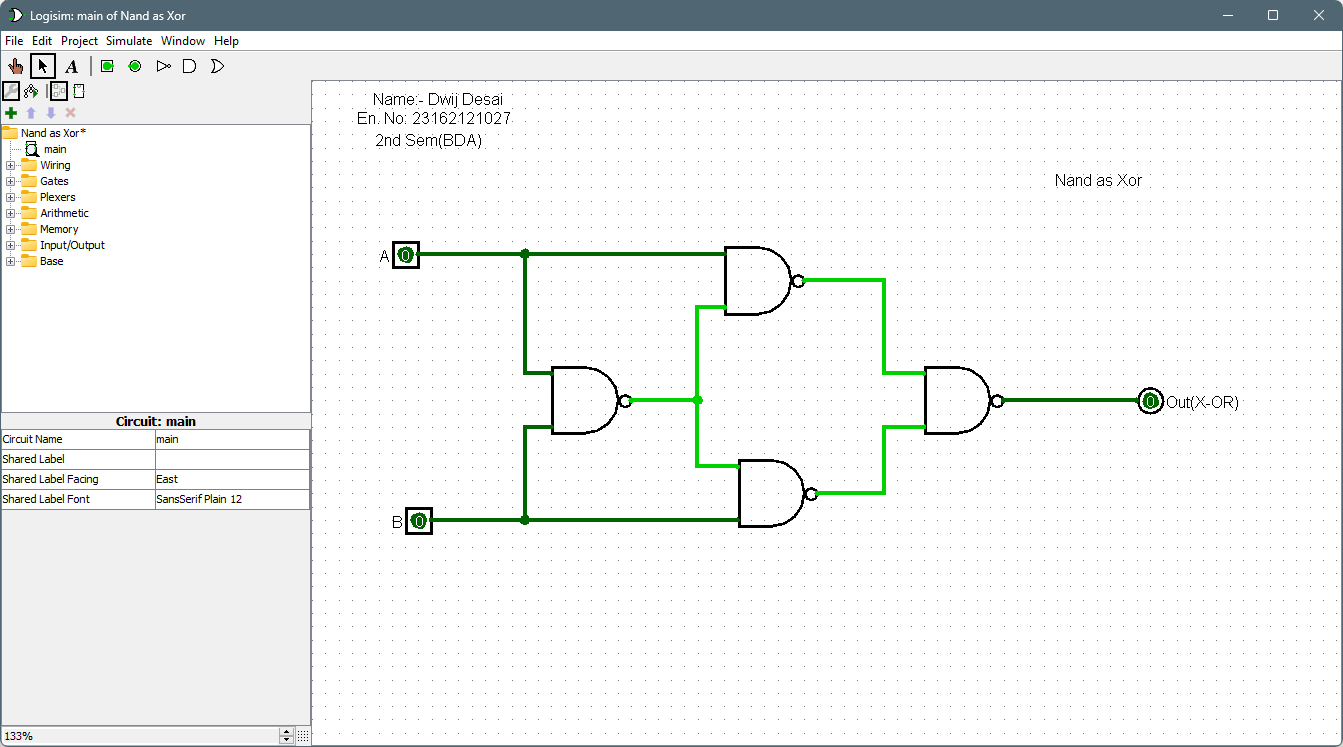


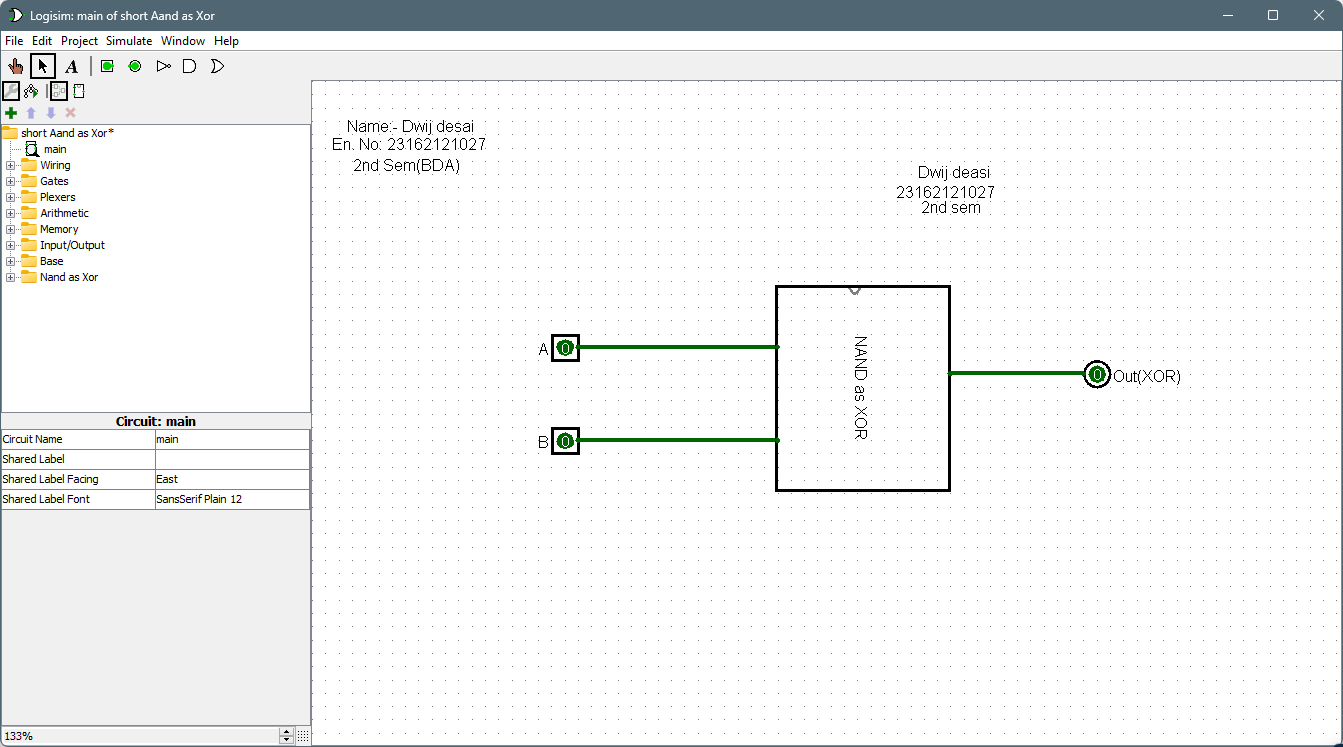
1. Boolean(A'[B+C]):-



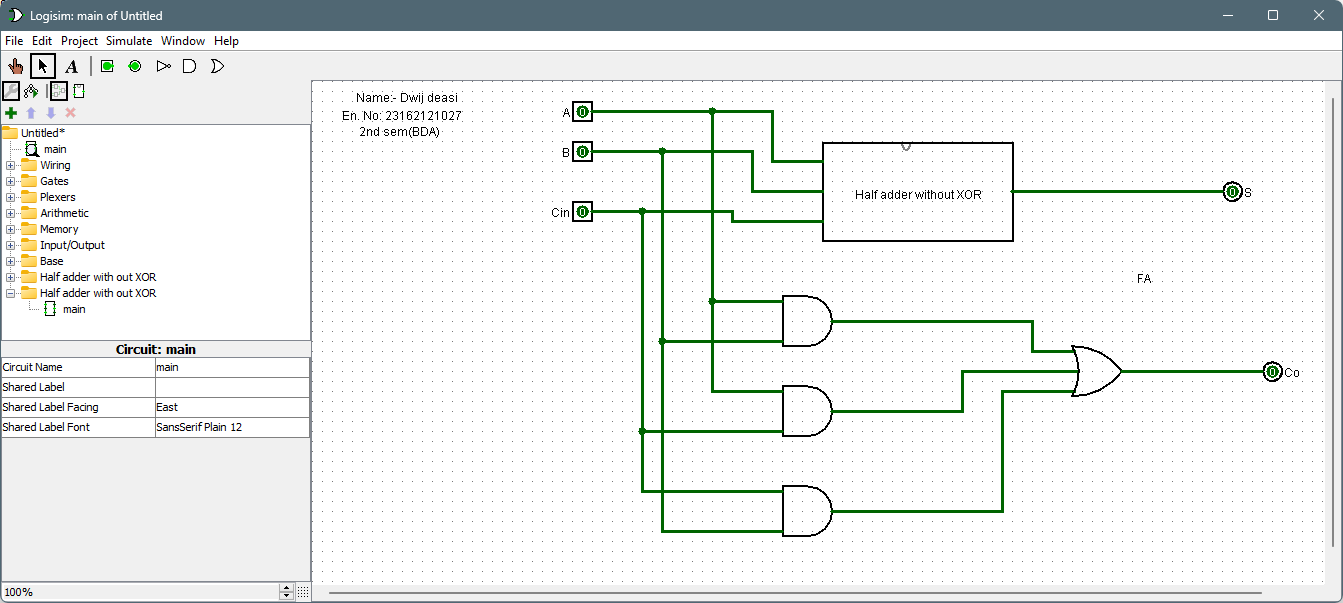


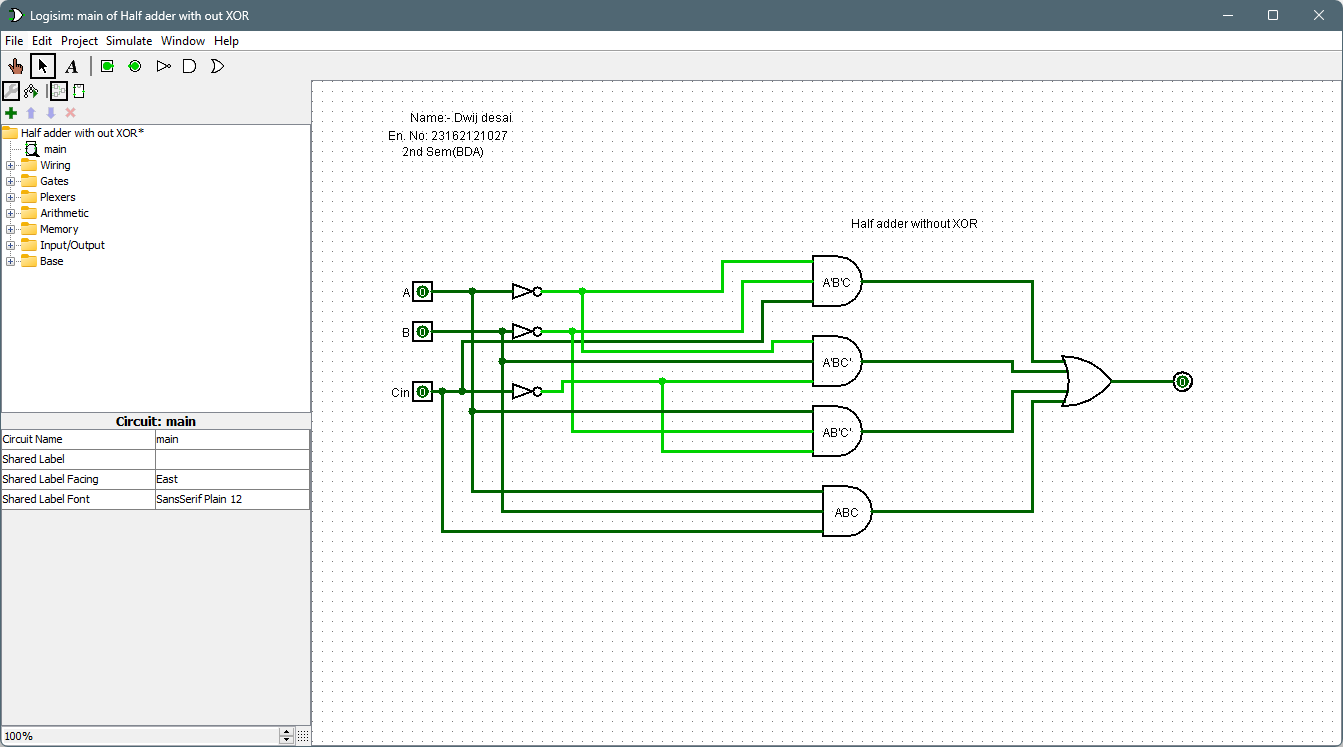
1. NAND as XOR:-





1. Full adder without XOR:-





* **Conclusion:-**

*Using "Edit View" in Logisim allows for the modification of a circuit's appearance and organization without altering its functionality, aiding in clarity, organization, and customization of complex circuits.*

